



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Ferro et al.

Serial No.: 10/060,750

Group Art Unit: 2123

Filed: January 30, 2002

Examiner: Unknown

For: SYSTEM FOR CONTROLLING EXTERNAL MODELS USED FOR VERIFICATION
OF SYSTEM ON A CHIP (SOC) INTERFACES

Assistant Commissioner of Patents
Washington, D.C. 20231

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SUBMISSION OF FORMAL DRAWINGS

Sir:

Submitted herewith are two (2) sheets of formal drawings comprising Figs. 1- 2 for the above-referenced patent application. Acknowledgment of receipt is respectfully requested. Please substitute these formal drawings for the drawings which were filed with the application.

Respectfully Submitted,

Frederick W. Gibbs, III
Reg. No. 37,629

Date: 4/8/02
McGinn & Gibb, PLLC
2568-A Riva Road, Suite 304
Annapolis, Maryland 21401
(410) 573-1545
Customer No. 29154

2123

CERTIFICATE OF MAILING BY FIRST CLASS MAIL (37 CFR 1.8)

Applicant(s): Ferro et al.

Docket No.

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Examiner

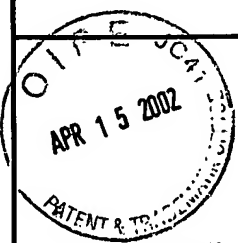
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Group Art Unit

2123

Invention: **SYSTEM FOR CONTROLLING EXTERNAL MODELS USED FOR VERIFICATION OF SYSTEM ON A CHIP (SOC) INTERFACES**

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I hereby certify that this Submission of Formal Drawings

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